2023 IEEE 15th International Conference on ASIC (ASICON)



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ASICON 2023

PROGRAM

Oct. 24th. - Oct. 27th., 2023

Platinum Hanjue Hotel, Nanjing, China



















2023 IEEE 15th International Conference on ASIC (ASICON)

ASICON 2023

Oct. 24th - Oct. 27th , 2023 Platinum Hanjue Hotel, Nanjing, Chna

Sponsored by

IEEE Beijing Section
Fudan University
Nanjing University
National IC Innovation Center

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Fudan University

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Welcome to ASICON 2023

Due to the challenges posed by the global COVID-19 pandemic and its profound impact on international travel, we were compelled to adapt ASICON 2021 into a virtual event. Today, we stand in a different landscape as the pandemic is behind us, allowing us to return to the tradition of hosting an in-person conference. On behalf of the ASICON 2023 organizing committee, it is our distinct pleasure and honor to extend a heartfelt welcome to all attendees. We sincerely appreciate your participation, which makes this event possible.

ASICON 2023 marks the 15th installment of this esteemed conference series, originating in 1994. Scheduled to take place from October 24th to 27th, 2023, in the picturesque city of Nanjing, China, we are eager to rekindle the spirit of collaboration and innovation that defines this gathering. With your active engagement and contributions, we aspire to make this year's in-person meeting even more engaging and successful.

While the term "ASIC" has traditionally had a narrower interpretation, ASICON has embraced a broader definition, signifying Advanced Semiconductor Integrated Circuits. This shift acknowledges the comprehensive technical scope that ASICON encompasses within the realm of integrated circuits. The conference serves as a global platform where VLSI circuit designers, ASIC users, System Integrators, IC manufacturers, device engineers, and CAD/CAE tool developers come together to share their latest advancements, developments, and research findings. It is also a hub where academics and industry professionals converge to foster networking and exchange valuable information.

ASICON 2023 has thoughtfully curated a program that includes five expert-led tutorials on the inaugural day of the conference. Furthermore, we are honored to announce the participation of eight world-renowned academic and industry leaders who will deliver keynote speeches during the plenary sessions from October 25th to 27th.

Over the years, ASICON has significantly impacted both industry and academia, serving as a catalyst for progress and collaboration. We are eager to uphold this venerable tradition and look forward to achieving new milestones at this year's face-to-face conference.

Once again, a warm welcome to ASICON 2023. Let us unite, share, and innovate as we embark on this exciting journey of discovery and collaboration. Thank you for being an integral part of this remarkable event.

General Co-Chairs of ASICON 2023

Jan Van der Spiegel Zhiliang Hong Yong Lian Ting-Ao Tang Yi Shi Hongxia Liu

Oct. 24th, 2023

Conference Committee

Gerneral Co-Chairs		
Name	Affiliation	Country/Area
Jan Van der Spiegel	University of Pennsylvania	USA
Zhiliang Hong	Fudan University	China
Yong Lian	Shanghai Jiao Tong University	China
Ting-Ao Tang	Fudan University	China
Yi Shi	Nanjing University	China
Hongxia Liu	Xidian University	China

Advisory Committee Co-Chairs		
Chenming Hu	UC Berkeley	USA
Richard.M.M. Chen	IEEE Hong Kong Section	Hongkong, China
Hiroshi Iwai	Yang Ming Chiao Tung University	Taiwan, China
Cor Claeys	IMEC & KU Leuven	Belgium
Qianling Zhang	Fudan University	China

Program Committee Co-Chairs		
Fan Ye	Fudan University	China
Xinran Wang	Nanjing University	China
François Rivet	University of Bordeaux	France
Haruo Kobayashi	Gunma University	Japan
Hidetoshi Onodera	Kyoto University	Japan
Jyi-Tsong Lin	Sun Yat-sen University	Taiwan, China

Yi Zhao East China Normal University Chi
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Organizing Committee Co-Chairs		
Mengqi Zhou	IEEE Beijing Section	China
Huihua Yu	Fudan University	China

Publicity Co-Chairs		
Rui Yin	National IC Innovation Center	China
Wei Xu	Fudan University	China
Jiting Sheng	Fudan University	China

	Secretary-General	
Fan Ye	Fudan University	China

Technical Program Committee Members of ASICON 2023

	Analog and RF Circuits Subcommittee	
Chen, Wei-Zen	Yang Ming Chiao Tung University	Taiwan, China
Lee, Tai-Cheng	Taiwan University	Taiwan, China
Zhang, Feng	Institute of Microelectronics, CAS	China
Kobayashi, Haruo	Gunma University	Japan
Simon, Ang	University of Arkansas	USA
Huang, Mo	University of Macau	Macao, China
Song, Fei	Ubilinx technology, Inc	USA

Institute of Semiconductor, CAS	China
Intel	USA
Shanghai Jiaotong University	China
Zhejiang University	China
Delft University of Technology	Netherlands
Nankai University	China
Eindhoven University of Technology	Netherlands
Digital Circuits and SOC Subcommittee	
University of Maryland	USA
University College Dublin	United Kingdom
Wenzhou University	China
Huazhong University of Science and Technology	China
NextInput, Inc.	USA
CEA Tech	France
Sun Yat-Sen University	Taiwan, China
University of Minnesota	USA
University of South Alabama	USA
Yang Ming Chiao Tung University	Taiwan, China
Yang Ming Chiao Tung University	Taiwan, China
Lund University	Sweden
Kookmin University	Korea
University of Tokyo	Japan
Sun Yat-sen University	China
Kyushu Institute of Technology	Japan
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Zhang, Chuan	Southeast University	China
	CAD Techniques Subcommittee	
Sheldon, Tan	University of California, Riverside	USA
Qu, Gang	University of Maryland	USA
Yu, Bei	Chinese University of Hong Kong	Hongkong, China
Jerraya, Ahmed	CEA Tech	France
Chan, Mansun	Hong Kong University of Science and Technology	Hongkong, China
Wang, Xingang	Skyworks Solutions, Inc.	USA
Wen, Xiaoqing	Kyushu Institute of Technology	Japan
	Process and Devices Subcommittee	
Chang-Liao, Kuei-Shu	Tsing Hua University	Taiwan, China
LAI, Chao-Sung	Chang Gung University	Taiwan, China
Kobayashi, Masaharu	The University of Tokyo	Japan
Chen, Kuan-Neng	Yang Ming Chiao Tung University	Taiwan, China
Jerraya, Ahmed	CEA Tech	France
Lee, Ching-Ting	Cheng Kung University/ Yuan Ze University	Taiwan, China
Li, Pei-Wen	Chiao Tung University	Taiwan, China
Zhao, Weisheng	Beihang University	China
Simon, Ang	University of Arkansas	USA
Ng, Wai Tung	University of Toronto	Canada
Simoen, Eddy	IMEC	Belgium
Endo, Kazuhiko	Advanced Industrial Science and	Japan

	Technology (AIST)	
Zhang, Jian Fu	Liverpool John Moores University	United Kingdom
Zhang, Weidong	Liverpool John Moores University	United Kingdom
Xie, Ya-Hong	UCLA	USA
Kong, Moufu	University of Electronic Science & Technology of China	China

General Information

Conference Language

The official language is English. No simultaneous translation is available.

Conference Schedule

Date	Time	Event
Oct. 24 th .	AM	Tutorial Session (T1)
Tue.	PM	Tutorial Session (T2)
	AM	Opening
		Keynote Session (K1, K2)
Oct. 25 th .		Parallel Sessions (A/B/C/D1)
Wed.	PM	Parallel Sessions (A/B/C/D2)
		Poster Session (P1)
	Evening	Reception
	AM	Keynote Session (K3)
Oct. 26 th .	AIVI	Parallel Sessions (A/B/C/D3)
Thur.		Parallel Sessions (A/B/C/D4)
1 Hul.	PM	Parallel Sessions (A/B/C/D5)
		Poster Session (P2)
	AM	Keynote Session (K4)
Oct. 27 th .	AWI	Parallel Sessions (A/B/C/D6)
	PM	Parallel Sessions (A/B/C/D7)
Fri.		Parallel Sessions (A/B/C/D8)
	Evening	Closing & Banquet

Conference Site

The conference will be held in Platinum Hanjue Hotel, Nanjing

Tel: (+86) 025-66008888

Add: No.888 Xuanwu Avenue, Nanjing, Jiangsu, China

Registration Desk

The conference registration desk will be located at Platinum Hanjue Hotel. The conference registration will be open on Oct. 24^{th} (8: $00\sim20$: 00), Oct. $25^{th}\sim$ Oct. 27^{th} (8: $00\sim17$: 45). And the registration desk will keep available at the same site throughout the whole conference.

Transportation

How to get to Nanjing Platinum Hanjue Hotel:

From Airport:

It takes about 53 minutes from Nanjing Lukou International Airport to Hotel by car. Taxi is recommended.

From Railway Station:

- * Nanjing Railway Station: Taxi takes about 15 minutes to hotel;
- * Nanjing South Railway Station: Taxi takes about 25 minutes to hotel

More details about the conference hotel booking, please visit

https://www.discoverchinatours.com/nanjing-asicon-2023-hotel.html

Weather

The average temperature during conference time in Nanjing is around $14^{\circ}\text{C} \sim 19^{\circ}\text{C}$.

Visa

All the foreign travelers to China must have a valid visa. Visas may be obtained from the Chinese Embassy or Consulate in most major cities around the world. A conference attendee will be mailed an official invitation letter for visa application after he or she fills and returns the Visa Application Form (http://www.asicon.org) to asicon_org@ fudan.edu.cn timely.

Awards

Excellent Student Paper Awards & Outstanding Young Scholar Paper Award will be announced at the banquet on Oct. 27th. To be qualified for these Best Paper Award, the paper must be presented by the student or scholar him- or herself (first author). The Technical Program Committee and Organizing Committee will choose best papers through public appraisal from the candidates.

Paper Presentation Information

The ASICON2023 will have oral and poster sessions. All the papers included in the conference program should be presented in English by one of the authors at the arranged sessions.

Oral Presentation

Presentation time:

Invited paper (25~30 minutes): 20~25 min talk + 5 min Q/A Regular paper (12~15 minutes): 10~12 min talk + 2~3 min Q/A Computer and digital projector will be provided in each meeting room.

Poster Presentation

Poster size: $120 \text{ cm (high)} \times 100 \text{ cm (wide)}$

Poster Session 1:

Setup time: 8: 30-17: 30 on Oct. 25th

Presentation time: 17: 45-18: 45 on Oct. 25th (on the spot)

Poster Session 2:

Setup time: 8: 30-17: 30 on Oct. 26th

Presentation time: 17: 45-18: 45 on Oct. 26th (on the spot)

Thumb pins, adhesive tapes, and scissors will be provided at the registration desk. The poster should be taken off by 21: 30 by the author if he or she would like to keep it. After that time, it will be removed and be regarded as being discarded by the authors.

Coffee Break

Complementary coffee/tea will be served in each morning/afternoon session. The break will take place in general at 10: 00-10: 15 during morning sessions and 15: 30-15: 45 during afternoon sessions. Due to time schedule of different sessions, the actual break time may have slight variation. Coffee/tea will be served in about half-hour duration.

Meeting Room Location

Meeting Room	Location
Grand Hall (Hall 210)	2 nd Floor, Platinum Hanjue Hotel
Hall 202	2 nd Floor, Platinum Hanjue Hotel
Hall 203	2 nd Floor, Platinum Hanjue Hotel
Hall 207	2 nd Floor, Platinum Hanjue Hotel
Hall 209	2 nd Floor, Platinum Hanjue Hotel

Tutorial Session

Tuesday

Tuesday, October 24, 9: 00 – 18: 15

Tuesday, October 24, 9: 00 – 12: 15

Hall 209

Tutorial Session T1

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Chixiao Chen, Fudan University, China

T-1 **Low-Power ADCs with Time-Domain Techniques** (9: 00-10: 30)

Prof. Qiang Li, University of Electronic Science and Technology of China, China

T-2 Hardware/Software Co-Design of Deep Learning Accelerators (10: 45-12: 15)

Prof. Yiyu Shi, University of Notre Dame, USA

Tuesday, October 24, 13: 30 – 18: 15

Hall 209

Tutorial Session T2

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Wenzhong Bao, Fudan University, China

Low-dimensional Semiconductors for High Performance, Low Power Electronics **T-3**

(13:30-15:00)

Prof. Yanqing Wu, Peking University, China

Electronics and Optoelectronics Based on 2D Tellurium (15: 00-16: 30) **T-4**

Dr. Chaoliang Tan, The University of Hong Kong, Hong Kong, China

Reliable In-memory Computing with Unreliable Devices and Circuits T-5

(16: 45-18: 15)

Prof. Yu Cao, University of Minnesota, USA

Technical Session

Wednesday

Wednesday, October 25, 9: 00 –10: 30

Wednesday, October 25, 9: 00 –10: 30

Hall 210

Opening & Keynote Session K1

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Bin Zhao, IEEE EDS, USA

K1-1 Technology Innovations at the Heart of Engineering Humanitarian Solutions (9: 00-9:45)

Dr. Rakesh Kumar, Technology Connexions, USA

K1-2 Let the Plants Do the Talking: Smart Agriculture by the Messages Received from Plants and Soil (9: 45-10: 30)

Prof. Danilo Demarchi, Politecnico di Torino, Italy

Wednesday, October 25, 10: 45–12: 15

Wednesday, October 25, 10: 45-12: 15

Hall 210

Keynote Session K2

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Jyi-Tsong Lin, Sun Yat Sen University, Taiwan, China

K2-1 Oxide Thin-Film Transistors and Integrations (10: 45-11: 30)

Prof. Aimin Song, University of Manchester, The United Kingdom

K2-2 Efficiency, Resilience, and Versatility in Memristive Neuromorphic Systems for AI on the Edge (11: 30-12: 15)

Prof. Gert Cauwenberghs, UC San Diego, USA

Wednesday, October 25, 13: 30 – 15: 30

Wednesday, October 25, 13: 30 – 15: 30

Hall 209

Session A1: Mixed-Signal Circuit I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Fan Ye, Fudan University, China

	Title	
A 1 1	0520: Utilizing Order Statistics for Low-Power Analog Circuit Design in Scaled	
A1-1	CMOS Technologies (invited)	
13:30	Mohfugul Islam (Vuota University Israel)	
~13:54	Mahfuzul Islam (Kyoto University, Japan)	
A1-2	0331: Ultra-Low-Power and High-Accuracy CMOS Temperature Sensor (invited)	
13:54	Jing Li, Luhan Yang, Dongjian Chen, Zhong Zhang, Qihui Zhang, Ning Ning, Qi Yu	
~14:18	(University of Electronic Science and Technology of China, China)	
A1-7	0285: CMOS Terahertz Detector and Image Sensor (invited)	
14:18~	Limon Lin (China Analama (China)	
14:42	Liyuan Liu (Chinese Academy of Sciences, China)	
A1 2	0295: A Region of Interest Technique for Event Driven Typed SPAD Readout	
A1-3	Circuit	
14:42	Minusi III. Changgang Wan Livia Thang lin Wu (Southaget University Ching)	
~14:54	Minwei Hu, Chenggong Wan, Lixia Zheng, Jin Wu (Southeast University, China)	
A1-4	0297: A SPAD Relative Address Coding for Lateral Resolution Improvement in	
A1-4	Coincidence Detection	
14:54	Chenggong Wan, Lixia Zheng, Jin Wu (Southeast University, China)	
~15:06	Chenggong Wan, Lixia Zheng, Jin Wu (Southeust University, China)	
A1-5	0333: A 64×64 active and passive imaging readout circuit based on	
A1-3	HgCdTe-LMAPD	
15:06	Rixian Tang, Ruiming Zhong, Jin Wu, Lixia Zheng (Southeast University, China)	
~15:18	Kixian Tang, Rumining Zhong, 3m wu, Lixia Zheng (Soumeust Oniversity, China)	
A1-6	0442: Loop Oscillation Analysis of MEMS Resonant Pressure Sensor Readout	
711-0	Circuit	
15:18	Tao Lu, Tao Yin, Wei Wang, Huan-ming Wu, Li-yuan Liu (Yunnan Normal University,	
~15:30	China; Institute of Semiconductors, Chinese Academy of Sciences, China; University	
~13.30	of Chinese Academy of Science, China; Ningbo University, China)	

Wednesday, October 25, 13: 30 – 15: 30

Session B1: Digital Circuit I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Yun Chen, Fudan University, China

Energy-efficient Approximate DCT Design for Image Processing
e Chen, Chenghua Wang, Weiqiang Liu (Nanjing University of
and Astronautics, China)
Performance Rejection Sampling Hardware Circuit Design for Kyber
Huihong Zhang, Yuejun Zhang, Hongshuai Wei, Pengjun Wang, Tengfei
jie Wang (Ningbo University, China; Wenzhou University, China)
rchitecture of a Single-Event Tolerant D Flip-flop Using Full-Custom nm Process
n, Yuejun Zhang, Huihong Zhang, Liang Wen, Pengjun Wang, Zhiyi Li
versity, China; China Coast Guard Academy, China; Wenzhou University,
ustom Design of Improved Carry Adder Circuit for CLBs
, Yuejun Zhang, Huihong Zhang, Liang Wen, Tengfei Yuan, Pengjun Li (Ningbo University, China; China Coast Guard Academy, China; versity, China)
of PUF Circuit Based on Charge Leakage of Cascade Dynamic Gate
Gang Li, Pengjun Wang (Wenzhou University, China)
n of Lightweight Strong Arbiter PUF Circuit Based on MOSFET oss
Xuejiao Ma, Gang Li (Wenzhou University, China; Wenzhou University
y, China)
ient Search Path Reduction for NB-LDPC Codes with T-EMS
n, Houren Ji, Xiaohu You, Chuan Zhang (Southeast University, China; tain Laboratories, China)
n,

Wednesday, October 25, 13: 30 – 15: 30

Session C1: Novel Device I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Chen Wang, Fudan University, China

C1-1	0480: Spintronic In-Memory-Computing: from Devices to Circuits (Invited)	
13:30	Vyo Zhong (Baihang University China)	
~14:00	Yue Zhang (Beihang University, China)	
C1-2	0482: Van Der Vaals Semiconductor Heterojunction Spintronic Devices (Invited)	
14:00	Voivou Wong (Institute of Somioon directors, Chinage Academy of Sciences, China)	
~14:30	Kaiyou Wang (Institute of Semiconductors, Chinese Academy of Sciences, China)	
C1-3	0486: Building a Spiking Sensory Neuron with Oxide-Based Neuromorphic	
C1-3	Devices (Invited)	
14:30	Mangijaa Pai, Chang lin Wan (Nanijaa Uniyawita, Ching)	
~15:00	Mengjiao Pei, ChangJin Wan (Nanjing University, China)	
C1-4	0489: Integrated Memristor Networks and Chips for Neuromorphic Computing	
C1-4	(Invited)	
15:00	Vuchao Vana (Pakina University China)	
~15:30	Yuchao Yang (Peking University, China)	

Wednesday, October 25, 13: 30 – 15: 30

Session D1: Processor

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Jun Han, Fudan University, China

	Title	
D1-1	0481: Exploring Machine Learning Adoption in Customisable Processor Design	
	(Invited)	
13:30	Jose G. F. Coutinho, Ce Guo, Tim Todman, Wayne Luk (Imperial College London,	
~13:54	The United Kingdom)	
D1-2	0535: Hardware Acceleration of Functional Encryption (invited)	
13:54~	Makoto Ikeda (The University of Tokyo, Japan)	
14:18	Makoto ikeda (The University of Tokyo, Japan)	
D1-3	0300: General Vector Instruction Extension for GF(2 ^m) Polynomial Operation in	
D1-3	Post-quantum Cryptography	
14:18	Honglin Kuang, Yifan Zhao, Yi Sun, Jun Han (Fudan University, China)	
~14:30	Hongini Kuang, Tilan Zhao, Ti Sun, Jun Han (Fudan University, China)	
D1-4	0316: MUG5: Modeling of Universal Chiplet Interconnect Express (UCIe)	
	Standard Based on gem5	
14:30	Xiaoyan Li, Zizheng Dong, Shuaipeng Li, Sai Gao, Jianfei Jiang, Guanghui He,	
~14:42	Zhigang Mao (Shanghai Jiao Tong University, China)	
D1-5	0374: Coupled Data Prefetch and Cache Partitioning Scheme for	
D1-9	CPU-Accelerator System	

14:42	Zengshi Wang, Chao Fu, Jun Han (Fudan University, China)
~14:54	Zerigeni Wang, emae 1 a, ean 1 an (1 maan emvelou), emaay
D1.6	0430: A Multi-mode Convolution Coprocessor Based on RISC-V Instruction Set
D1-6	Architecture
14:54	Wenqiang Gong, Fang Zhou, Fen Ge (Nanjing University of Aeronautics and
~15:06	Astronautics, China)
D1 =	0440 70 14 70 74 74 74 74 74 74 74 74 74 74 74 74 74
D1-7	0448: Permutation-Based Approximate Multiplier with High Accuracy
D1- 7	
	Kunlong Li, Yunfei Dai, Zhen Li, Lingli Wang (Fudan University, China)
15:06	
15:06	
15:06 ~15:18	Kunlong Li, Yunfei Dai, Zhen Li, Lingli Wang (Fudan University, China)
15:06 ~15:18 D1-8	Kunlong Li, Yunfei Dai, Zhen Li, Lingli Wang (Fudan University, China) 0484: Design of a Data Transmission Control Unit in a Multi-core DSP System

Wednesday, October 25, 15: 45-17: 45

Wednesday, October 25, 15: 45-17: 45
Session A2: Mixed-Signal Circuit II Platinum I

Platinum Hanjue Hotel 2nd Floor

Hall 209

Session Chair: Prof. Jing Li, University of Electronic Science and Technology of China, China

	Title		
A2-1	0252: Back to the Analog Neural Network and Linear Circuit Theory (Invited)		
	Haruo Kobayashi, Manato Hirai, Kakeru Otomo, Shogo Katayama, Xueyan Bai,		
15:45	Masashi Chiba, Zifei Xu, Dan Yao, Lengkhang Nengvang, Minh Tri Tran, Kanji		
	Yoshihiro, Anna Kuwana, Takato Ooide, Hiroshi Tanimoto, Yuji Gendai, Jianglin Wei		
~16:15	(Gunma University, Japan; Kitami Institute of Technology, Japan; Yibin University,		
	China)		
422	0536: A Sinusoidal Fitting-Based Digital Foreground Calibration Technique for		
A2-2	Pipelined ADC (Invited)		
16:15	Beicheng Xue, Zhifei Lu, Wei Zhang, He Tang, Xizhu Peng (University of		
~16:30	Electronic Science and Technology of China, China)		
A2-3	0305: A 59.99dB SNDR 1.13mW Ping-pong NS SAR ADC for 3-D		
A2-3	Transesophageal Echocardiography		
16:30	Jing Li, Tianci Zhang, Yingchen Liu, Penghao Jiang, Zhong Zhang, Qihui Zhang,		
~16:42	Ning Ning, Qi Yu (University of Electronic Science and Technology of China, China)		
A2-4	0341: Analysis and Modeling of Non-ideal Effects in SAR ADC		

16:42	Yaxin Zeng, Xi Feng, Hao Xu, Na Yan (Fudan University, China; Beijing Smartchip
~16:54	Semiconductor Technology Co., Ltd, China)
A2-5	0360: A 77.8dB-SNDR 25MHz-BW 2 nd -order NS Pipelined SAR ADC with
	4 th -order Gain-Error-Shaping
16:54	Guolong Fu, Li Tian, Yanbo Zhang, Shubin Liu, Zhangming Zhu (Xidian University,
~17:06	China)
12.6	0406: A 32GS/s 7bit TI-SAR ADC in 28nm for 32Gb/s ADC-Based SerDes
A2-6	Receiver
17:06	Jun Chen, Fengyi Mei, Mingzhe Liu, Yongzhen Chen, Jiangfeng Wu (Tongji
~17:18	University, China)
A2-7	0412: Pipelined-SAR ADC Calibration Technique Based on Gain-Bit Weights
17:18	Hang Ling, Yifei Bai, Fengyi Mei, Huajun Yao, Yongzhen Chen, Jiangfeng Wu (Tongji
~17:30	University, China)

Wednesday, October 25, 15: 45-17: 45

Session B2: Digital Circuit II

Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Chuan Zhang, Southeast University, China

	Title
B2-1	0222: Design of Multi-Mode Digital Signal Processing Circuit for Digital Transmitters
15:45 ~16:00	Changgu Yan, Yun Yin, Hongtao Xu (Fudan University, China)
B2-2	0356: A Speed Up Method towards DDR Subsystem Functional Verification in SoC
16:00 ~16:15	Yande Jiang, Na Chen, Huiquan Wang, Guangda Zhang, Jun Xia, Xiaobo Yan (Academy of Military Sciences, China; Beijing University of Technology, China; Nanhu Laboratory, China)
B2-3	0384: A Decision-Based CORDIC Hardware for Arc Tangent Calculation
16:15 ~16:30	Haoyu Wu, Liyu Lin, Haodong Sun, Xiaoyang Zeng, Yun Chen (Fudan University, China)
B2-4	0398: Ternary Multiply-Accumulate Circuit Based on Domino Structure
16:30 ~16:45	Hanyu Shi, Yuejun Zhang, Huihong Zhang, Qikang Li, Pengjun Wang (Ningbo University, China; Wenzhou University, China)
B2-5	0399: A 7nm-Based Decodable Self-Resetting Regfile Circuit
16:45	Wanlong Zhao, Yuejun Zhang, Mingze Ren, Liang Wen, Pengjun Wang (Ningbo

~17:00	University, China; China Coast Guard Academy, China; Wenzhou University, China)		
B2-6	0405: An Efficient Hash Computing Unit for Kyber Algorithm		
17:00	Hongshuai Wei, Yuejun Zhang, Huihong Zhang, Yang Wang, Tengfei Yuan, Chengjie		
~17:15	Wang, Pengjun Wang (Ningbo University, China; Wenzhou University, China)		
D2 5	0472: Hardware Implementation of Chromatic Dispersion Compensation in		
B2-7	Finite Fields		
17:15	Zhenhao Ji, Ruiyang Ji, Mingyuan Ding, Xiangning Song, Xiaohu You, Chuan Zhang		
~17:30	(Southeast University, China; Purple Mountain Laboratories, China)		
D2 0	0474: Low-Complexity GAI-BP Detection for MIMO Systems with		
B2-8	Threshold-updating Strategy		
17:30	Wenyu Huang, Yifan Shi, Wenyue Zhou, Jiaqian Ling, Xiaohu You, Chuan Zhang		
~17:45	(Southeast University, China; Purple Mountain Laboratories, China)		
1			

Wednesday, October 25, 15: 45-17: 45

Session C2: Novel Device II

Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Ziyu Liu, Fudan University, China

	Title
C2-1	0259: Complementary Field-Effect Transistors: From Silicon to 2D Materials
C2-1	(Invited)
15:45	Mansun Chan (The Hong Kong University of Science and Technology, Hong Kong,
~16:09	China)
C2-2	0525: Atomic LEGO for Future Computing (Invited)
16:09	Feng Miao (Nanjing University, China)
~16:33	Teng Miao (wanjing Oniversity, China)
C2-3	0478: Silicon Based 2D Flash Memory (Invited)
16:33	Pana Thou (Fudan University China)
~16:57	Peng Zhou (Fudan University, China)
C2-4	0502: Hybird 2D/CMOS Microchips for Memristive Applications (Invited)
16:57	Mario I ango (Vina abdullah University of Saignes and Technology Saudi Anghia)
~17:21	Mario Lanza (King abdullah University of Science and Technology, Saudi Arabia)
C2-5	0515: Defect and Interface Engineering of Two Dimensional Materials (Invited)
17:21	Thombus Ni (Couthough University China)
~17:45	Zhenhua Ni (Southeast University, China)

Wednesday, October 25, 15: 45-17: 45

Session D2: SoC

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Ziyu Guo, Fudan University, China

	Title
D2-1	0490: Scalable Highly Integrated Quantum Bit Error Correction System by
D2-1	Classical Electronics (Invited)
15:45	Vozutoshi Vohovoshi (Vvoto Institute of Tashuelogy Isnau)
~16:15	Kazutoshi Kobayashi (Kyoto Institute of Technology, Japan)
D2-2	0500: A Non-Centralized Routing Scheme with Phase Caching CDR for Nanosecond-Level Optical Switching Systems (Invited)
16:15 ~16:45	Xin Lu, Heng Zhang, Leilei Wang, Tao Fang, Chunhui Zhang, Feng Wang, Yashe Liu, Xiangfei Chen, Li Du, Yuan Du (Nanjing University, China; Huawei Tech. Co., Ltd, China)
D2-3	0416: A low-power daisy-chain controller implemention in BMS based on power mode switching
16:45	Vinhoo Vy Vonozhon Chon Lionofono Wy (Touri: University Ching)
~17:00	Xinhao Xu, Yongzhen Chen, Jiangfeng Wu (Tongji University, China)
D2-4	0464: Peripheral Hardware System Design for a Neuromorphic Chip
17:00	Wang Shi, Jian Cao, Guang Chen, Xuan Wang, Shengrong Liu, Yawei Ding (Peking
~17:15	University, China)

Wednesday, October 25, 17: 45 – 18: 45

Wednesday, October 25, 17: 45 –18: 45

Poster Session I

Platinum Hanjue Hotel 2nd Floor

	Title
P1-1	0203: Design of Analog Front-end for Human Resistance Monitoring Technology
	Zitong Zhu, Wensi Wang, Wenjing Wang (Beijing University of Technology, China)
P1-2	0213: An Ultra-low-power Temperature Sensor with an Accuracy of +0.6/-1 °C
I 1-2	from -30 ℃ to 90 ℃
	Hanyang Wang, Zhonghan Shen, Hao Min (Fudan University, China; Shanghai
	Quanray Electronics CO. LTD, China)
P1-3	0227: Multi-channel 600V-level Driver for Piezoelectric-Electrohydrodynamic
P1-3	Hybrid Inkjet Printer
	Jae-Hyoun Park (Korea Electronics Technology Institute, Korea)

P1-4	0243: A DC Offset Cancellation Circuit Using Digital Assistance Technique and Self-Calibrating Comparator for RF Transceiver
	Zhiyuan Cao, Zirui Jin, Dongsheng Liu, Chengcheng Zhang (Huazhong University of
	Science and Technology, China)
	0246: An Improved Frequency Compensation Scheme for a Low Quiescent
P1-5	Current Low Dropout Voltage Regulator with Wide Input Voltage and Load Current Range
	Wenjun Li, Bingjie Chen, Jianhua Feng (Peking University, China)
P1-6	0271: A Dual-mode Broadband Image Sensor Based on Graphene-CMOS
	Integration Ye Lin, Yang Xiao, Jingjing Lv, Li Du, Yuan Du (Nanjing University, China)
P1-7	0313: An Adaptive Current Source IGBT Gate Driver Based on Current and Voltage Slope Feedback to Reduce EMI
	Chang Liu, Shuohan Yang, Qingyue Zhou, Run Min, Desheng Zhang, Yinyu Wang,
	Shuo Zhang, Qiaoling Tong (Huazhong University of Science and Technology, China)
P1-8	0315: A Bandgap Voltage Reference with Low Temperature Coefficient and High
	PSRR Designed for LDO
	Yuzi Wang, Xichen Duan, Kai Sun, Peng Huang, Liuyang Zhang, Jie Liang (Shanghai University, China)
	Chiversity, China)
P1-9	0324: A Fully-Integrated Analog Front-End for Carbon-Based Short-Wave
	Infrared Image Sensor
	Weirong Xi, Jianhua Jiang, Chengying Chen (<i>Xiamen University of Technology, China; Peking University, China</i>)
	China, 1 eking Oniversity, China)
P1-10	0328: Design of Smooth Mode Transition Buck-Boost Converter Based on Adaptive Offset Cancellation
	Shenhao Jiang, Hao Chen, Shaowei Zhen, Keyu Li, Xin Chen, Liang Huang,
	Yongsheng Du, Bo Zhang (University of Electronic Science and Technology of China,
	China; Suplet Co., Ltd., China)
D1 11	0347: A High Precision Capacitive Isolation Amplifier for Current Sensing
P1-11	Applications
_	Yonghui Wu, Yiwei Liu, Shaowei Zhen, Yanliang Li, Yikang Li, JiaNing Zhang, Yi
	Ou, Bo Zhang (University of Electronic Science and Technology of China, China;
	Chongqing Optoelectronics Research Institute, China)
	0351: A Low Power Consumption and Higher Performance DDR5 Receiver

	DFE
	Elaine Tang, Chris Eom, Jake Jung, Brian Lee (Design center CXMT, China)
P1-13	0352: Pseudo Differential DQS Receiver for Eliminating Channel Hi-z Noise
	Xueyan Zhang, Chris Eom, Jake Jung, Brian Lee, Gaoyuan Pang (Design center
	CXMT, China)
P1-14	0426: A High-Throughput Luma Mapping with Chroma Scaling Decoder for Versatile Video Coding
	Zekai He, Wei Li, Leilei Huang, Yibo Fan (Fudan University, China; East China
	Normal University, China)
P1-15	0410: A Cost-efficient Hybrid Gate Driver For SiC MOSFETs and IGBTs
	Yue Shi, Jinyang He, Zhijian Zhang, Zekun Zhou, Bo Zhang (University of Electronic
	Science and Technology of China, China; Chengdu University of Information
	Technology, China)
P1-16	0422: An Improved Delay Cell with Low Power Consumption and Strong Driving Capability
	Cai Tian, Shunli Ma, Wenzhong Bao, Tianxiang Wu (Fudan University, China)
P1-17	0429: A High Precision Current Sampling Circuit with Rail-to-Rail
F1-1/	Common-Mode Input Range
	Zekun Zhou, Yun Dai, Jianli Lou, Yue Shi, Bo Zhang (University of Electronic Science
	and Technology of China, China; Chengdu University of Information Technology)
P1-18	0434: A High Precision CMOS Temperature Detector with Curvature Calibration Technique
	Weizhen Cai, Xiaobo Chen, Xiaoming Liu, Jianjun Zhou (Shanghai Jiao Tong
	University, China)
P1-19	0438: A High-precision Current Detection Circuit for Battery Management
	System De Con We Heavier December 1 if We 7 houses 7 hours 7 houses 7 hours 7
	Pu-Sen Wu, HaoXue, Byambajav Ragchaa, LiJi Wu, Zhenhui Zhang, Xiangmin Zhang
	(Heilongjiang University, China; Tsinghua University, China; Beijing National
	Research Center for Information, Science and Technology, China)
D4 60	0458: A PSR Enhancement Scheme: An Overview of Feed-Forward Ripple
P1-20	Cancellation Technique
	Wentao Zheng, Xiaohang Wang, Libo Qian (Ningbo University, China; Xidian
	University, China)
P1-21	0245: Linearity Analysis for Charge Domain In-memory Computing
	Heng Zhang, Yuan Du, Li Du (Nanjing University, China)

P1-22	0269: A Low-Delay Self-Interference Cancellation Chip with Channel Sounding Capability
	Jiarui Chen, Shunyang Chen, Menglei Zhu, Xiaoguo Huang, Guangqi Zhen (Science
	and Technology on Communication Information Security Control Laboratory, China)
P1-23	0311: High Frame Rate High Precision ROIC with Pixel-level CCO-Based ADC for Infrared FPAs
	Haolin Lu, Ye Zhou, Wengao Lu, Yacong Zhang, Zhongjian Chen (<i>Peking University</i> , <i>China</i> ; <i>Beijing Advanced Innovation Center for Integrated Circuits</i> , <i>China</i>)
P1-24	0314: A 128-electrodes Neural Probe with 30*55 μm² Channel Area Low-power CCO-based ADC
	Weixiong Qiu, Shihui Sun, Yufei Ai, Wengao Lu, Yacong Zhang, Zhongjian Chen (Peking University, China; Beijing Advanced Innovation Center for Integrated
	Circuits, China)
P1-25	0321: A Pattern Cancel DAC system design methodology for FMCW radar
11-25	Yue Lin, Hongtao Xu (Fudan University, China)
P1-26	0367: A CT DSM with DAC Scaling Technique for Direct Neural Recording Front-End
	Yuekai Liu, Jinlei Pan, Liang Qi (Shanghai Jiao Tong University, China)
P1-27	0370: A Low-Complexity Timing Skew Mismatch Calibration Method for Time-Interleaved ADCs
	Sujuan Liu, Shibo Li, Xudong Sun (Beijing University of Technology, China)
P1-28	0371: A Transient-Enhanced Digital-LDO With Adaptive Clock-Edge Control
	Guoqiang Song, Wenxin Yan, Junhui Zhang, Lin He (Nanjing University of Posts and Telecommunications, China)
P1-29	0387: Dual Code Channel Hybrid Readout Circuit Based on High Precision Photoelectric Encoders
	Feng-Wei Wang, Yun-Hao Fu, Yu-Chun Chang, Fei Wang, Dong-Xu Zhao (University
	of Chinese Academy of Sciences, China; Jilin University, China; Changchun Institute
	of Optics, Fine Mechanics and Physics, Chinese Academy of Sciences, China)
P1-30	0418: High Performance Bootstrap Switch for 14 bit SAR ADC with Redundancy in SMIC 180nm
	Jing Yuan, Tianxiang Wu, Shunli Ma, Wenzhong Bao (Fudan University, China)
P1-31	0419: A 300MS/s 57.6dB SNDR Single-Channel SAR ADC with Accelerated SAR Logic
	Logic

	Muxi Zou, Xiaodi Feng, Tianxiang Wu, Shunli Ma, Junyan Ren (Fudan University,
	China)
P1-32	0420: A Multi-channel 12-bits 100MS/s SAR ADC in 65nm CMOS
	Yigang Wei, Tianxiang Wu, Shunli Ma, Junyan Ren (Fudan University, China)
	0423: A High Gain and Wide Bandwidth Dual-Power CMOS Op-amp for
P1-33	High-Speed ADCs Application
	Xiaodi Feng, Muxi Zou, Tianxiang Wu, Shunli Ma (Fudan University, China)
P1-34	0457: A Novel 16-bit ADC Based on Third-order Σ-Δ Modulator with Zero
P1-34	Optimization
	Yanming Li, Mengyao Liu, Lufang Zhang (Chang'an University, China)
P1-35	0253: A Broadband Voltage Controlled Oscillator with Multi-Band Output
	Boming Su, Sikai Chen, Peiyin Cai, Tao Peng, Yi Wu, Guochi Huang (Fujian Normal
	University, China; Key Laboratory of OptoElectronic Science and Technology for
	Medicine of Ministry of Education, China; Fujian Provincial Engineering Technology
	Research Center of Photoelectric Sensing Application, China)
P1-36	0318: A Driver Amplifier with Configurable Transformer Based Matching
	Networks in 65-nm CMOS
	Hangbiao Li, Ran Zhang, Kai Zhang, Xiaodong Zhao, Zhiqing Liu and Shuai Liu
	(Southwest China Institute of Electronics Technology, China)
P1-37	0421: A 15GHz Class-C VCO with Two-stage Buffer in 0.15-μm GaAs
1107	Lei Wu, Tianxiang Wu, Shunli Ma, Junyan Ren (Fudan University, China)
	201 Way Talantang Way State Tal, Carly and Tear (2 main Conversity), Contrary
P1-38	0431: Fast locking Sampling PLL Using Phase Error Eliminator
	Shengyuan Zhou, Chao Yang, Sheng Wang, Ziyao Xia, Xiaoming Liu, Jing Jin
	(Shanghai Jiao Tong University, China)
P1-39	0433: A Wideband Inductorless LNA Employing Dual-Loop Feedback for
	Low-Power Applications
	Zhaolin Yang, Yuyang Chen, Xiaoming Liu, Jing Jin, Jianjun Zhou (Shanghai Jiao
	Tong University, China)
	0436: A 30GHz Bidirectional PA/LNA with Transformer-Based Switchable RC
P1-40	Matching Network
	Hanqi Gao, Zhaolin Yang, Xiaoming Liu, Jing Jin, Jianjun Zhou (Shanghai Jiao Tong
	1/niversity (.nina)
	University, China)
P1-41	0208: A 10Gbps high-speed \ low-noise optical receiver based on CMOS 45nm

	Wenli Liao, Daifa Gao, Chengying Chen, Yufei Huang (Xiamen University of
	Technology, China)
P1-42	0435: A 24/48 Gb/s NRZ/PAM-4 Dual-Mode Transmitter with 3-tap FFE in 28
F1-42	nm CMOS
	Jiaxu Zhou, Yichao Lin, Bo Wang, Jing Jin, Shan Wang, Tingting Mo (Shanghai Jiao
	Tong University, China; Montage Technology Co. Ltd., China; SJTU-Montage IC
	Design Frontier Technology Joint Lab, China)
P1-43	0529: A NOVEL PROGRAMMABLE RESISTANCE AND CAPACITANCE
	NETWORK FOR HIGH-PRECISION ANALOG DESIGN
	Zhu Kejia (Common Mode Semiconductor, China)

Thursday

Thursday, October 26, 8: 30 – 10: 00

Thursday, October 26, 8: 30 – 10: 00

Hall 210

Keynote Session K3

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Haruo Kobayashi, Gunma University, Japan

K3-1 RF Acoustic Wave Devices in Mobile Communications --- Aliens from Jupiter (8: 30-9: 15)

Prof. Ken-ya Hashimoto, University of Electronic Science and Technology of China, China

K3-2 The Back-gate of UTBB FDSOI Transistor: a Magic Knob for Analog and Mixed Cells (9: 15-10: 00)

Prof. Gilles Jacquemod, Université Côte d'Azur, France

Thursday, October 26, 10: 15 – 12: 15

Thursday, October 26, 10: 15 – 12: 15

Hall 209

Session A3: Analog Circuit I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Haruo Kobayashi, Gunma University, Japan

	Title
A3-1	0493: Transmitter IC Enabling Magnetic Field Shaping for High-Efficiency Wireless Charging of Multiple Receivers(invited)
10:15	Hao Qiu, Xusheng Zhang, Junji Chen, Yi Shi, Makoto Takamiya (Nanjing University)
~10:45	China; The University of Tokyo, Japan)
~10.43	China, The University of Tokyo, Jupan)
A3-2	0235: A 23-nA Quiescent Current Output-Capacitorless LDO Regulator for IoT Devices
10: 45	Shengnan Zhou , Cheng Huang, Rui P. Martins, Yan Lu, Xiangyu Mao (University of
~11:00	Macau, Macao, China; Iowa State University, Ames, USA)
A3-3	0268: A Low Ripple Frequency-Feedback PFM-PWM Buck Converter with Seamless Mode Transition
11:00	Zhong Zhao ,Bo Zhang, Ping Luo,Zhiyuan Zhang; Jiahang Fan, Hao Chen
~11:15	(University of Electronic Science and technology of China, China)
A3-4	0298: Current Balancing Strategy based on Threshold Midpoint Adjustment for Interleaved Constant Frequency Hysteresis Control Buck Converter
11:15 ~11:30	Yinyu Wang, Wenjun Tang, Desheng Zhang, Run Min, Shuo Zhang, Wenxuan Tan, Wanyang Wang, Liying Zhu, Chang Liu, Qiaoling Tong (Huazhong University of Science and Technology, China; Beijing Academy of Space Technology, China)
A3-5	0309: An Analog Assisted Dual Loop Hybrid LDO Based on Adaptive Clock
11:30	Xichen Duan, Yuzi Wang, Peng Huang, Kai Sun, Liuyang Zhang, Jie Liang (Shangha)
~11:45	University, Shanghai, China; Peng Cheng Laboratory, China)
A3-6	0319: A Fast-Transient Right-Half-Plane Zero-Free Hybrid Buck-Boost Converter
11:45 ~12:00	Hao Chen, Shenhao Jiang, Yajuan He, Hailiang Xiong, Xin Chen, Hongyang Wu, Liang Huang, Yongsheng Du, Bo Zhang, Shaowei Zhen (University of Electronic Science and Technology of China, China; Suplet Co., Ltd., Beijing, China)
A3-7	0456: Sub-50mV Bootstrap Clock Booster and Integrated Cold Start for Thermoelectric Energy Harvesting
	Haizhun Wang, Xiudeng Wang, Yinshui Xia (Ningbo University, China; Xidian
12:00	Thaizman Wang, Madeing Wang, Thisman Ma (Wingoo Chiversity, China, Matta

Thursday, October 26, 10: 15 – 12: 15

Session B3: AI Circuit I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Runsheng Wang, Peking University, China

	Title
B3-1	0485: Mitigating Non-Ideality Issues of Analog Computing-In-Memory In
D3-1	DNN-Based Designs(invited)
10:15	Chi-Tse Huang, An-Yeu Wu (Taiwan University, Taiwan, China)
~10:40	g,,,,,,
	0500 P. J. J. W. J. J. J. G. ED/2D J. J.
B3-2	0508: Benchmarking Heterogeneous Integration with 2.5D/3D Interconnect Modeling(invited)
10:40	Zhenyu Wang, Jingbo Sun, Alper Goksoy, Sumit K. Mandal, Jae-sun Seo, Chaitali
~11:05	Chakrabarti, Umit Y. Ogras, Vidya Chhabria, and Yu Cao(Arizona State University,
	USA; University of Wisconsin-Madison, USA; Indian Institute of Science, India)
В3-3	0216: An 842nW Wearable Inter-Patient Cardiac Arrhythmia Monitoring
11:05	Processor with a Feature Engine-Based Artificial Neural Network
~11:17	Zihao Ye, Xuecong Lu, Shuai Wang, Bing Li (Shenzhen University, China)
_	0231: An Area-Power-Efficient Multiplier-less Processing Element Design for
B3-4	CNN Accelerators
11:17	Jiaxiang Li, Masao Yanagisawa, Youhua Shi (Waseda University, Japan)
~11:29	Jianiang El, Masao Tanagisawa, Tounda Sin (Maseau Oniversity, Japan)
В3-5	0292: A Domain-Specific DMA Structure for Per-channel Processing-based CNN Accelerator
11:29	Yi Chen, Mengni Bie, Tao Chen, Longmei Nan, Yiran Du, Wei Li (Information
~11:41	Engineering University, China)
B3-6	0323: A 28nm 15.09nJ/inference Neuromorphic Processor with SRAM-Based
11 41	Charge Domain in-Memory-Computing
11:41	Yuchao Zhang, Zihao Xuan, Yi Kang (University of Science and Technology of
~11:53	China, China)
	0334: UACT: A Unified Energy-efficient Computing Architecture for CNN and
B3-7	TCNN
11:53	Yufan Chen, Xuyang Duan, Jun Han (Fudan University, China)
~12:05	Total Chen, Zuyung Duan, Jun Han (1 maan Omversny, China)

Thursday, October 26, 10: 15 – 12: 15

Session C3: Power & Compound Device I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Moufu Kong, University of Electronic Science and

Technology of China, China

	Title
C3-1	0521: Hetero-Integration of Ga ₂ O ₃ Bipolar Devices Toward Power
	Electronics(invited)
10:15	Hehe Gong, Jiandong Ye (Nanjing University, China)
~10:45	Tiene Gong, Jiandong Te (Ivanjing University, China)
C3-2	0229: Inversion-Mode InGaAs FinFETs for Logic and RF Applications(invited)
10:45	Jing-Yuan Wu, Mu-Yu Chen; Edward. Yi Chang (Yang-Ming Chiao-Tung University,
~11:15	Taiwan, China)
C3-3	0503: A Scalable Compact Model for High-Frequency GaN-HEMTS(invited)
11:15	Xing Zhou, Siau Ben Chiah (Nanyang Technological University, Singapore; New
~11:45	Silicon Corporation Pte Ltd, Singapore)
C3-4	0214: An Ultra-Low Specific On-Resistance LDMOS With Segmented LOCOS In 0.18µm BCD Process Platform
11:45	Jun Huang, Ning Ning, Renxiong Li, Qi Ding, Yutuo Guo, Yu Wang, Kunqin He,
~12:00	Yaxin Liu, Lulu Peng (United Microelectronics Center Co., Ltd., China)
C3-5	0337: A Highly Automated and Rapid Datasheet Driven Empirical Modeling Process of SiC MOSFETs with High Accuracy and Robust Convergence
12:00 ~12:15	Zhenbo Rao, Yan Wang (Tsinghua University, China)

Thursday, October 26, 10: 15 – 12: 15

Session D3: FPGA
Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Jinmei Lai, Fudan University, China

	Title
D2 1	0273: OpenPARF: An Open-Source Placement and Routing Framework for
D3-1	Large-Scale Heterogeneous FPGAs with Deep Learning Toolkit(invited)
10:15	Jing Mai, Jiarui Wang, Zhixiong Di, Guojie Luo, Yun Liang, Yibo Lin (Peking
~10:45	University, China; Southwest Jiaotong University, China; Beijing Advanced
	Innovation Center for Integrated Circuits, China)
D3-2	0219: A Low-complexity Max Unpooling Architecture for CNNs

10:45	Xiaojun Zhang, Chenshi Zhu, Qin Han, Zhengrong Wang, Dexue Zhang (Shandong
~11:00	University of Science and Technology, China; State key Laboratory of High-end
711.00	Server and Storage Technology, China)
D3-3	0262: Hardware Acceleration Linear Matrix Solvor Based on FPGA
11:00	Dui Chi Vunfan Zua Valana Zhana Haa Van (Cauthanat Huisanaita, China)
~11:15	Rui Shi, Yunfan Zuo, Kelong Zhang, Hao Yan (Southeast University, China)
D2 4	0280: Efficient FPGA Routing Architecture Exploration Based on Two-Stage
D3-4	MUXes
11:15	Jide Zhang, Kaixiang Zhu, Kaichuang Shi, Hao Zhou, Lingli Wang (Fudan
~11:30	University, China)
D3-5	0395: High-Performance BLS12-381 Pairing Engine on FPGA
D3-5	
	O395: High-Performance BLS12-381 Pairing Engine on FPGA Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>)
11:30	
11:30	
11:30 ~11:45 D3-6	Anawin Opasatian, Makoto Ikeda (The University of Tokyo, Japan)
11:30 ~11:45 D3-6 11:45	Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>) 0407: A Compilation Toolchain of Neural Networks for FPGA Backend
11:30 ~11:45 D3-6	Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>) 0407: A Compilation Toolchain of Neural Networks for FPGA Backend Jun Zeng, Panfeng Wang, Haili Wang, Fuchun Sun, Hailong Yao (<i>Tsinghua</i>)
11:30 ~11:45 D3-6 11:45	Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>) 0407: A Compilation Toolchain of Neural Networks for FPGA Backend Jun Zeng, Panfeng Wang, Haili Wang, Fuchun Sun, Hailong Yao (<i>Tsinghua University, China; Hercules Microelectronics Co., Ltd., China; University of Science</i>
11:30 ~11:45 D3-6 11:45	Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>) 0407: A Compilation Toolchain of Neural Networks for FPGA Backend Jun Zeng, Panfeng Wang, Haili Wang, Fuchun Sun, Hailong Yao (<i>Tsinghua University, China; Hercules Microelectronics Co., Ltd., China; University of Science</i>
11:30 ~11:45 D3-6 11:45 ~12:00	Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>) 0407: A Compilation Toolchain of Neural Networks for FPGA Backend Jun Zeng, Panfeng Wang, Haili Wang, Fuchun Sun, Hailong Yao (<i>Tsinghua University, China; Hercules Microelectronics Co., Ltd., China; University of Science and Technology Beijing, China</i>)
11:30 ~11:45 D3-6 11:45 ~12:00	Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>) 0407: A Compilation Toolchain of Neural Networks for FPGA Backend Jun Zeng, Panfeng Wang, Haili Wang, Fuchun Sun, Hailong Yao (<i>Tsinghua University, China</i> ; Hercules Microelectronics Co., Ltd., China; University of Science and Technology Beijing, China) 0415: An Accurate Area Model for FPGA Circuits at advanced technologies
11:30 ~11:45 D3-6 11:45 ~12:00 D3-7 12:00	Anawin Opasatian, Makoto Ikeda (<i>The University of Tokyo, Japan</i>) 0407: A Compilation Toolchain of Neural Networks for FPGA Backend Jun Zeng, Panfeng Wang, Haili Wang, Fuchun Sun, Hailong Yao (<i>Tsinghua University, China; Hercules Microelectronics Co., Ltd., China; University of Science and Technology Beijing, China</i>) 0415: An Accurate Area Model for FPGA Circuits at advanced technologies Yanze Li, Jianfan Zhang, Zhichao Wei, Jian Wang, Jinmei Lai (<i>Fudan University</i> ,

Thursday, October 26, 13: 30 – 15: 30

Thursday, October 26, 13: 30 – 15: 30

Session A4: Analog Circuit II

Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Hao Qiu, Nanjing University, China

	Title
A4-1	0340: Passiveless Digitally Controlled Oscillator With Embedded PVT Detector
	Using 40-nm CMOS (invited)
13:30	Ralph Gerard B. Sangalang, You-Wei Shen, Shiva Reddy, Lean Karlo S. Tolentino,
	Chua-Chin Wang (Sun Yat-Sen University, Taiwan, China; The National Engineering
~13:55	University, Philippines; Technological University of the Philippines, Philippines)
A4-2	0516: A Bang-Bang Phase Detector for PAM-N Signaling(invited)
13:55 ~14:20	Johar Abdekhoda, Li Wang, Reza Sarvari, Chik Patrick Yue (The Hong Kong
	University of Science and Technology, Hong Kong, China; Sharif University of
	Technology, Iran)

A4-3	0463: Design of Chip-to-PCB Matching Network for Millimeter-Wave On-Chip
	Transmitter and On-PCB Antenna (invited)
14:20	Zilu Liu, Li Wang, Hamed Fallah, C.Patrick Yue (The Hong Kong University of
~14:45	Science and Technology, Hong Kong, China)
A4-4	0210: A Low Jitter Current-Mode Multiplying Delay-Locked Loop Applied to High-Precision TDC
14:45 ~15:00	Jin Sun, Jiahao Hu, Ziqi Song, Qing Li, Dian He, Hujun Jia (Xidian University, China)
A4-5	0342: An ADPLL Design Model Based on LoRa IoT Application
15:00	Yiyun Mao, Haoyun Gao, Dejian Li, Hao Xu, Na Yan (Fudan University, China;
~15:15	Beijing Smartchip Semiconductor Technology Co., Ltd, China)
A4-6	0343: A Vernier Time-to-Digital Converter with 1.5ps Resolution for an
A4-0	All-Digital Phase Locked Loop in 28nm CMOS
15:15	Peifang Wu, Yan Liu, Xi Feng, Hao Xu, Na Yan (Fudan University, China; Beijing
~15:30	Smartchip Semiconductor Technology Co., Ltd, China)

Thursday, October 26, 13: 30 – 15: 30

Session B4: AI Circuit II

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Yuhui He, Huazhong University of Science and Technology,
China

	Title
B4-1	0230: A Unifying Tensor View for Lightweight CNNs(invited)
13:30	Jason Chun Lok Li, Rui Lin, Jiajun Zhou, Edmund Yin Mun Lam, Ngai Wong (The
~14:00	University of Hong Kong, Hong Kong, China)
B4-2 0495: Hardware-Specific Optimization for Mapping of Convolutiona	
DT-2	Networks to Memristor Crossbars(invited)
14:00	Seokjin Oh, Rina Yoon, Seungmyeong Cho and Kyeong-Sik Min (Kookmin
~14:30	University, Korea)
B4-3	0233: A Time- And Energy-Efficient CNN With Dense Connections On
D4-3	Memristor-Based Chips
14:30	Wenyong Zhou, Yuan Ren, Jiajun Zhou, Tianshu Hou, and Ngai Wong (The
~14:45	University of Hong Kong, Hong Kong China; Shanghai Jiao Tong University, China)
B4-4	0312: An Optimized Dataflow Based Accelerator for Sparse Convolutional
D4-4	Neural Networks
14:45	Xuran Ding, Guowang Su, Jun Zhang (Central South University, China)

~15:00	
B4-5	0350: Loop-Tiling Based Compiling Optimization for CNN Accelerators
15:00	Meiling Yang, Shan Cao, Wei Zhang, Yu Li, and Zhiyuan Jiang (Shanghai University,
~15:15	China)
B4-6	0441: A Dynamic Codec with Adaptive Quantization for Convolution Neural
	Network
15:15	Yichen Ouyang, Xianglong Wang, Gang Shi, Lei Chen, Fengwei An (Southern
~15:30	University of Science and Technology, China)

Thursday, October 26, 13: 30-15: 30 Hall 203 **Session C4: Power & Compound Device II** Platinum Hanjue Hotel 2^{nd} Floor **Session Chair: Prof. Rui Jin, Beijing Institute of Smart Energy, Huairou Laboratory, China**

	Title
C4-1	0488: Processes of p-GaN Gate HEMTs for High-efficiency and High-reliability
	Applications(invited)
13:30	Junting Chen, Chengcai Wang, Zuoheng Jiang, Mengyuan Hua (Southern University
~14:00	of Science and Technology, China)
C4 2	0522: Recess-Patterned Ohmic Contact Technology for AlGaN/GaN
C4-2	Heterostructures(invited)
14:00	Xinyi Tang, Yang Jiang, Fangzhou Du, Nick Tao, Qing Wang, Hongyu Yu (Southern
~14:30	University of Science and Technology, China; The University of Hong Kong, Hong
~14.30	Kong, China; Maxscend Microelectronics Company Limited, China)
C4-3	0277: A Novel SiC Superjunction Trench MOSFET with Integrated
	Heterojunction Diode for Improved Performance
14:30	Moufu Kong, Ronghe Yan, Bingke Zhang, Ke Huang, Bo Yi, Hongqiang Yang
~14:45	(University of Electronic Science and Technology of China, China)
C4-4	0462: Comprehensive Comparison of Temperature Performances for SiC Trench
C4-4	MOSFET with Integrated Side-wall Schottky Diode and Heterojunction
14:45	Bo Yi, Haoran Hu, Yilin Guo, Junji Cheng, Haimeng Huang, MouFu Kong, WenKun
	Shi, HongQiang Yang (University of Electronic Science and Technology of China,
~15:00	China; China Zhenhua Group Yong guang Electronics CO.LTD, China)

Thursday, October 26, 13: 30 – 15: 30 Hall 207 **Session D4: EDA I** Platinum Hanjue Hotel 2nd Floor **Session Chair: Prof. Zhaori Bi, Fudan University, China**

	Title
D4-1	0236: Logic Synthesis for Emerging Technologies(invited)
13:30	Giovanni De Micheli (EPFL, Lausanne, Switzerland)
~14:00	Giovanni De Michell (El PL, Lausanne, Switzertana)
	0528: TED Analog Circuit Optimization Framework: Toward Fully Automated
D4-2	Analog Design (invited)
14:00~	Yuan Wang, Qingsen Wu, Jian Xin, Qian Qin, Jinglei Hao, Xiongbo Zhang, Yuefan
14:30	Wang, Lin Li, Zuochang Ye, Zhiping Yu, Yan Wang (Tsinghua University, China;
	Xiamen University, China)
<u> </u>	0256: An Analytical Model for Domain-Specific Accelerator Deploying Sparse LU
D4-3	Factorization
14:30	
~14:45	Shuaibo Huang, Jiang Sha, Longxing Shi (Southeast University, China)
D4-4	0301: HDDB: a High Density Digital Waveform Storage Method
14:45	Biwei Liu, Jiageng Shi, Wencheng Jiang, Zhenyu Zhao, Jie Zhou (National University
~15:00	of Defense Technology, China)
D4-5	0349: An Efficient Scheduling Algorithm for Stream Computing
15:00	vo-77. An Emicient Scheduling Algorithm for Stream Computing
~15:15	Kexin Wang, Jundong Xie, Yiwei Wang, Chang Wu (Fudan University, China)
13.13	
D4-6	0379: HierSyn: Fast Synthesis for Large Hierarchical Designs
15:15	Yishan Zhang, Zhiyong Zhang, Chang Wu (Fudan University, China; Shanghai Fudan
~15:30	Microelectronics Group Co., Ltd, China)

Thursday, October 26, 15: 45 – 17: 45

Thursday, October 26, 15: 45 – 17: 45

Session A5: Analog Circuit III

Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Jiawei Xu, Fudan University, China

	Title
A5-1	0281: A Pseudo Short-circuit Adaptive Zero Current Detection Method for
A5-1	SIBTO in AMOLED Driver
15:45	Ziyuan Chu, Zehua Chen, Taijia Zhang, Xinyi Li, Yuyin Sun, Yimeng Zhang, Yuming
~16:00	Zhang (Xidian University, China)
A5-2	0282: A 0.69% LED Current Error LED Driver with Hysteretic Current Control

Zehua Chen, Ziyuan Chu, Taijia Zhang, Xinyi Li, Yuyin Sun, Yimeng Zhang, Yuming
Zhang (Xidian University, China)
0303: A 256-channel 11-bit OLED Source Driver IC with Unit Current
Calibration
Charish and May Viscon Cost Harras Li (Pail and Hair and China)
Shuaichen Mu, Xiaoyu Guo, Hongge Li (Beihang University, China)
0335: A 6-Gb/s Wireline Transmitter Design with 3-Tap FFE in 28nm CMOS
Technology
Dingrong Lyny Fon Vo. Junyon Don (Fudan University China)
Bingrong Lyu, Fan Ye, Junyan Ren (Fudan University, China)
0391: A 115-325MHz Wideband Analog Baseband with 0.5dB-Step Variable Gain
Amplifier and Six-order Reconfigurable Gm-C Lowpass Filter
Wen Zuo, Wei Li, Yun Wang, Yue Lin, Hongtao Xu (Fudan University, China; Zhuhai
Fudan Innovation Institute, China; ICLegend Micro, China)
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Thursday, October 26, 15: 45 – 17: 45

Session B5: AI Circuit III

Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Yufeng Xie, Fudan University, China

	Title
B5-1	0224: Optimizing Supervised Learning of Deep Spiking Neural Network towards
	Memristor Crossbar Implementation(invited)
15:45	Qi Chen, Dayou Zhan, Jiawei Fu, Yuhui He (Huazhong University of Science and
~16:15	Technology, China)
B5-2	0479: Not your father's stochastic computing (SC)! Efficient yet Accurate
D3-2	End-to-End SC Accelerator Design(invited)
16:15	Meng Li, Yixuan Hu, Tengyu Zhang, Renjie Wei, Yawen Zhang, Ru Huang, Runsheng
~16:45	Wang (Peking University, China; Beijing Advanced Innovation Center for Integrated
~10.43	Circuits, China)
B5-3	0205: A Model-Guided Underwater Image Enhancement Network
16:45	Leiou Wang, Donghui Wang (Chinese Academy of Science, China; University of
~17:00	Chinese Academy of Sciences, China)
B5-4	0238: Nonlinear modeling of MIMO antenna array power amplifiers based on
D3-4	time-delay neural network
17:00	Yiwei Zhou, Weibo Li, Yongzhen Chen (Tongji University, China)
~17:15	11wei Zhou, weloo El, Toligzhen Chen (Toligh Oniversity, China)

B5-5	0400: A Performance-driven Neural Network Compiler for Multi-core
	Computing-In-Memory Accelerator
17:15	Bokai Zeng, Chen Yang, Hui Zhao, Xiang Qiu (Xi'an Jiaotong University, China;
~17:30	Flash Billion Semiconductor Co. Ltd., China)
D5 (0404: A High-Performance YOLOV5 Accelerator for Object Detection with Near
B5-6	
	Sensor Intelligence
17:30	
17:30 ~17:45	Sensor Intelligence Jiacheng Cao, Ziyi Yang, Jie Lu, Jinmei Lai (Fudan University, China)

Thursday, October 26, 15: 45 – 17: 45

Session C5: Power & Compound Device III

Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Xinnan Lin, Anhui Polytechnic University, China

	Title
C5-1	0206: Tradeoff Between the Breakdown Voltage and Specific On-Resistance of SOI RESURF LDMOS (invited)
15:45	Yufeng Guo, Kemeng Yang, Jing Che, Man Li, Zhengfei Jiang, Jiafei Yao, Jun Zhang,
~16:15	Maolin Zhang (Nanjing University of Posts and Telecommunications, China)
C5-2	0393: An Ultra-low Specific On-resistance SiC LDMOS Using Double RESURF and Field Plate Techniques(invited)
	Moufu Kong, Ning Yu, Jiaxin Guo, Zeyu Cheng, Rui Jin, Hongqiang Yang (University
16:15	of Electronic Science and Technology of China, China; Smart Energy Research Centre
~16:45	Huairou Laboratory, Future Science City, China)
C5-3	0451: Optimal design of short circuit robustness for high voltage and high power IGBTs(invited)
16:45	Rui Jin, Ruifen Nie, Niannian Ge, Baohua Tian, Xiamin Hao, Feng He (Beijing
~17:15	Institute of Smart Energy, Huairou Laboratory, China; State Grid Shanghai Electric
~17.13	Power Research Institute, China)
C5 4	0389: A Novel 1200-V Class SiC MOSFET With Schottky Barrier Diode for
C5-4	Improved third quadrant performance
17:15	Moufu Kong, Hongfei Deng, Rui Jin, Zhi Lin, Bo Yi, Hongqiang Yang (University of
~17:30	Electronic Science and technology of China, China; Smart Energy Research Centre
~17.50	Huairou Laboratory, Future Science City, China; Chongqing University, China)
05.5	0413: Temperature Dependent Optimization for Specific On-Resistance for 900 V
C5-5	Superjunction MOSFETs: Numerical Calculation and Comparison
17,20	Zonghao Zhan, Xi Wang, Keqiang Ma, Siliang Wang, Chenxing Wang, Haoyang
17:30	Zhou, Haimeng Huang, Junji Cheng, Bo Yi, Hongqiang Yang (University of Electronic
~17:45	Science and technology of China, China)

Thursday, October 26, 15: 45 – 17: 45	Hall 207
Session D5: EDA II	Platinum Hanjue Hotel 2 nd Floor
Session Chair: Prof. Giovanni De Micheli, EPFL, Lausanne, Switzerland	

	Title
D5-1	0362: Full-Chip Voltage Prediction via Graph Attention Based Neural Networks
	(invited)
15:45	Yuan Li, Pingqiang Zhou (Duke Kunshan University, China; ShanghaiTech University,
~16:15	China)
D5-2	0373: OpenILT: An Open Source Inverse Lithography Technique
D5-2	Framework(invited)
16:15	Su Zheng, Bei Yu, Martin Wong (Chinese University of Hong Kong, Hong Kong,
~16:45	China)
D5-3	0257: Finding All Solutions of Multi-terminal Numberlink Problem Utilizing
บ5-3	Top-down ZDD Construction
16:45	Xuanqi Li, Takashi Imagawa, Hiroyuki Ochi (Ritsumeikan University, Japan; Meiji
~17:00	University, Japan)
D5-4	0266: Effective Analytical Placement for Advanced Face-to-Face-Bonded Circuit
D3- 4	Designs
17:00	Vuon Wen Zhijia Cai, Yingsu Tong, Min Wei, Jianli Chen (Fudan University, China)
~17:15	Yuan Wen, Zhijie Cai, Xingyu Tong, Min Wei, Jianli Chen (Fudan University, Chin

Thursday, October 26, 17: 45 - 18: 45 Thursday, October 26, 17: 45 - 18: 45 Poster Session II Platinum Hanjue Hotel 2nd Floor

	Title
P2-1	0207: Cost-Efficient Soft Error Detection and Correction Flip-Flop Design for
P2-1	Nanoscale Technology
	Hong-Chen Li, He Liu, Jie Li (Heilongjiang University, China; Harbin Institute of
	Technology, China)
P2-2	0237: A Digital Receive Beamforming IC for High-Frequency Ultrasound
P2-2	Imaging System
	Duo Sheng, Ying-Chi Chiu, Yun-Quan Li, You-Ning Lo, Chao-Kai Pai, and Ten-Ling
	Wang (Fu Jen Catholic University, Taiwan, China)

P2-3	0247: A Spike-Sorting-Assisted Compressed Sensing Processor for High-Density Neural Interfaces
	Qingzhen Wang, Wenxian Gu, Hengchang Bi, Liangjian Lyu, Deli Qiao, Xing Wu
	(East China Normal University, China)
P2-4	0279: FPGA Implementation of High Critical Sparsity Orthogonal Matching Pursuit Algorithm for Compressed Sensing Reconstruction
	Sujuan Liu, Jiajun Ma, Yichen Liang (Beijing University of Technology, China)
P2-5	0338: Periodic Analysis of Adaptive LMS Filter in TIADC
	Jiankun Li, Zepeng Lin, Fan Ye (Fudan University, China)
P2-6	0344: Design and Implementation of a Special Operator for Neural Networks
1 2-0	Based on Noise Reduction and Super Resolution
	Hongli Tian, Xiaodi Xing, Jian Zhang, Shaodi Wang, Yuan Wang (Peking University,
	China; Beijing Zhicun (Witmem) Technology Co., Ltd. China; Beijing Advanced
	Innovation Center for Integrated Circuits, China)
P2-7	0383: A Dynamic-Texture-Guided Fast Algorithm for Geometric Partitioning Mode of VVC
	Xuehang Yang, Wei Li, Shushi Chen, Leilei Huang, Yibo Fan (Fudan University,
	China; East China Normal University, China)
P2-8	0397: A Common Architecture for Digital Process of Ultrasonic Imaging System after AFE
	Chongzheng Fang, Chenhui Zhou, Fan Ye (Fudan University, China)
P2-9	0409: Complexity-Reduced Joint Calibration for Nonlinearity and I/Q Imbalance in Direct Conversion Transmitter
	Weibo Li, Minghao Jiang, Yongzhen Chen, Jiangfeng Wu (Tongji University, China)
P2-10	0439: A Deep Q Network Hardware Accelerator Based on Heterogeneous Computing
	Guohui Zhang, Fen Ge, Fang Zhou (Nanjing University, China)
P2-11	0447: A Low-power digital automatic gain control design in wireless communication receivers
	Jiangshan Zhao, Jiankun Huang, Yongzhen Chen, Jiangfeng Wu (Tongji University,
	China)
P2-12	0455: A Low-Complexity Algorithm for JPEG-LS-Based RAW Domain
F Z-1Z	Compression
	Yeping Zheng, Tingting Li, Wei Li, Faxing Lei, Jiarui Liu, Yibo Fan (Fudan
	University, China)

	0468: A Method of Mapping Convolutional Neural Networks on Resource-limited
P2-13	NoC Platform
	Jiantao Ye, Fen Ge, Fang Zhou (Nanjing University, China)
P2-14	0471: Low Complexity Belief-selective Massage Passing (BsMP) Detector for SCMA Systems
	Zhuangzhuang You, Xu Pang, Wenyue Zhou, Chao Ji, Xiaohu You, Chuan Zhang (Southeast University, China)
P2-15	0475: Improved GAI-BP Detection for MIMO Systems Based on Message Post-processing
	Ruiyang Ji, Wenyue Zhou, Xiaosi Tan, Xiaohu You, Chuan Zhang (Southeast
	University, China)
	0270: Design and Implementation of High-speed Reconfigurable Multi-core
P2-16	Network Security Protocol Analyse Processor
	Chen Guang, Li Binglong (Information Engineering University, China)
P2-17	0450: Rabbit: An Efficient Verification Platform Base on Virtual Peripherals
	Zhengyi Zhang, Yuanda Yang, Lingli Wang (Fudan University, China)
P2-18	0260: Performance Error Evaluation of gem5 Simulator for ARM Server
	Yudi Qiu, Shiyan Yi, Minge Jing, Xiankui Xiong, Dong Xu, Xuanpeng Zhu, Xiaoyang
	Zeng, Yibo Fan (Fudan University, China; ZTE Corporation, China)
P2-19	0261: FlsGraph: A Parallel Architecture for Large-scale Graph Processing
	Haohan Zhang, Song Cheng, Yi Kang (University of Science and Technology of
	China, China)
P2-20	0242: Memory-Efficient Compression Based on Least-Squares Fitting in
P2-20	Convolutional Neural Network Accelerators
	Hang Xu, Chenjia Xie, Xin Lu, Li Du, Yuan Du (Nanjing University, China)
P2-21	0272: A Reusable AI acceleration Architecture based on Matrix Multiplication for Convolutional Neural Network with Digital Signal ProcessingTasks
	Bisheng Chen, Xiayu Li, Jicheng Lu, Jun yu (Fudan University, China; Shanghai
	Fudan Microelectronics Group Co., Ltd, China)
D4 66	0200 A N G L 1000
P2-22	0308: An NoC-based CNN Accelerator for Edge Computing
	Jianing Gao, Qiming Shao, Fangyu Deng, Qin Wang, Naifeng Jing, Jianfei Jiang
	(Shanghai Jiao Tong University, China)
D2 22	0461, DCCMNoDE, Donald Colf J. MYC N-DE
P2-23	0461: DSSMNeRF: Depth Self-supervised MVS NeRF

	Yixuan Tong, Gengsheng Chen, Wei Xu (Fudan University, China)
	. 6, 6 7, (((), 6)
P2-24	0264: A Digital Clock and Data Recovery Architecture with Precise Voting for Multi-Gigabit/s Links
	Kaifan Jiang, Jun Yu (Fudan University, China)
	0310: High-Performance Genomic Analysis Heterogeneous System Using
P2-25	OpenCL
	Jianing Gao, Lingyi Liu, Qin Wang, Naifeng Jing, Jianfei Jiang (Shanghai Jiao Tong
	University, China)
	0320: Optimizing Wirelength And Delay of FPGA Tile through Floorplanning
P2-26	Based on Simulated Annealing Algorithm
	Honghong Long, Yanze Li, Jinmei Lai, Jian Wang (Fudan University, China)
P2-27	0353: A Fast-Lock DLL with Prediction-Based Fast-Track FDL Structure for DDR5 SDRAMs
	Gaoyuan Pang, Jake Jung, Chris Eom, Brian Lee (Design center, CXMT, China)
	0248: Lithographic Hotspot Detection Using Adaptive Squish Pattern Sampling
P2-28	Combined with Faster RCNN
	Jian Cui, Jian Zhang, Xuexiang Wang (Southeast University, China)
	0254: An Enhanced Packing Algorithm for FPGA Architectures without Local
P2-29	Crossbar
	Yuanqi Wang, Kaichuang Shi, Lingli Wang (Fudan University, China)
P2-30	0348: A General-Purpose Compiler Design for Instruction-Based AI Accelerator
	Implementation Management Wang Vivon Lingby Chang Wiv (Fielder University China: Shanghai
	Mengxuan Wang, Yuan Linghu, Chang Wu (Fudan University, China; Shanghai Fudan Microelectronics Group Co., Ltd, China)
	2 man Mario Control Con Day Con Day China)
P2-31	0417: An Automatic Optimization Method of Combinational Logic Loops in CGRA
	Mingyang Chen, Yunhui Qiu, Kaixiang Zhu, Lingli Wang (Fudan University, China)
P2-32	0459: Efficient Layout Pattern Matching Based On Local Information
	Wuxin Ge, Chao Wang (Southeast University, China)
P2-33	0467: Automatic Timing-Driven Top-Level Hardware Design for Digital Signal Processing
	Wuqiong Zhao, Changhan Li, Zhenhao Ji, You You, Xiaohu You, and Chuan Zhang (Southeast University, China)
	(22)

P2-34	0487: Integration Of Micro Surface Mount Components On Printed Circuit
	Board By micro-Transfer Printing
	Qiang Cheng, ZhaoCong Wang, YingXong Song, Jian Chen, QianWu Zhang, Nan Ye
	(Shanghai university, China)
P2-35	0221: Investigation of electrical characteristics of a novel FeFET-based relaxation
1 2-33	oscillator
	Chenyang Li, Chunsheng Jiang, Hongying Chen (Guangxi Normal University, China)
P2-36	0258: A Novel TFET-MOSFET Hybrid SRAM for Ultra-Low-Power Applications
F 2-30	Renjie Wei, Kaifeng Wang, Zhixuan Wang, Libo Yang, Fangxing Zhang, Yongqin Wu,
	Ye Ren, Le Ye, Lining Zhang, Weihai Bu, Ru Huang, Qianqian Huang (Peking
	University, China; Semiconductor Technology Innovation Center (Beijing), China;
	Chinese Institute for Brain Research, China; Beijing Advanced Innovation Center for
	Integrated Circuits, China)
	megratea Circuits, China)
	0332: Monolithic Logic Units based on DCFL Structure on p-GaN platform for
P2-37	GaN ICs
	Maolin Pan, Qiang Wang, Yuhang Wang, Luyu Wang, Penghao zhang, Min Xu (Fudan
	University, China)
D2 20	0465: A Novel Semi-superjunction SiC Trench MOSFET with Ultra-low Specific
P2-38	On-resistance
	Zhaoyu Ai, Xinyang Chen, Yuxi Zhou, Haiyun Liu, Jing Feng, Moufu Kong
	(University of Electronic Science and Technology of China, China)
D2 20	0215: Study on the Performance of Flexible Curved Inverted-F Antenna under
P2-39	Compound Deformation Condition
	Xiangyu Dai, Jinghui Li, Zhengfang Qian (Shenzhen University, China)
P2-40	0365: Glass Wet Deep Etching for Fabricating Biomimetic Devices in Biosensing
	Yuxin Li, Jie Wang, Zijian Zhou, Jiayi Wu, Ming Yang, Enqi Wu and Lin Du
	(University of Shanghai for Science and Technology, China)
	ONCE A MALLY SCALAR LANGE OF THE ANALYSIS OF THE PROPERTY OF T
P2-41	0265: A Modeling Study: Applying Carbon-Based Interconnects to BS-PDN Architecture
	Baohui Xu, Rongmei Chen, Jie Liang (Shanghai University, China; Interuniversity
	Microelectronics Centre (IMEC), Leuven, Belgium)
P2-42	0283: Design and Optimization of Ternary Inverter using Face Tunnel
	Field-Effect Transistor
	Aoxuan Wang, Hongliang Lu, Yuming Zhang, Jiale Sun, Yi Zhu (Xidian University,
	China)

Friday

Friday, October 27, 8: 30 - 10: 00

Thursday, October 27, 8: 30 – 10: 00

Hall 210

Keynote Session K4

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Zhiliang Hong, Fudan University, China

K4-1 Sub-Terahertz Communication and Its Future towards 6G (8: 30-9: 15)

Prof. Minoru Fujishima, Hiroshima University, Japan

K4-2 Terahertz-Chip-Scale Systems for Intelligent Sensing and 6G Communication

(9: 15-10: 00)

Prof. Kaushik Sengupta, Princeton University, USA

Friday, October 27, 10: 15–12: 15

Friday, October 27, 10: 15 – 12: 15 Hall 209

Session A6: Bio Circuit Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Yongzhen Chen, Tongji University, China

	Title
A6-1	0240: Frontier Applications Research for Next-Generation Cardiovascular Health Monitoring Chip Design (invited)
10:15~ 10:40	Hsientsai Wu (Dong Hwa University, Taiwan, China)
A6-2	0491: An Integrated System of Blood Pressure and Electrocardiograph Recordings for Smart Home Healthcare Network (invited)
10:40~ 11:05	Feng Zou, Hai Huang, Ye Yuan, Yuhua Cheng (Peking University, China; Hangzhou Mixchips Microelectronics Co., Ltd. China)
A6-3	0386: A Three-stage Analog Low-Frequency Drift Calibration and DC Offset Correction Circuit for Ultrasonic AFE (invited)
11:05~ 11:30	Fan Ye, Siqing Wu, Xinwei Yu, Xingtao Zhu, Junyan Ren (Fudan University, China)
A6-4	0276: A High Linearity Large Time Constants Switched-Resistor Filter for Biomedical Applications
11:30~ 11:45	Yajie Zhao, Yizhou Jiang, Weiming Hu, Yajie Qin (Fudan University, China)
A6-5	0306: A Programmable High-Voltage Pulse Transmitter Circuit for 3-D Miniature Ultrasound Probes
11:45~	Jing Li, Penghao Jiang, Tianci Zhang, Yingchen Liu, Zhong Zhang, Qihui Zhang,
12:00	Ning Ning, Qi Yu (University of Electronic Science and Technology of China, China)
A6-6	0339: A 23.5μA Ultra-Low Standby Power Microphone ASIC with the Voice Activity Detection Based on A Level-Crossing ADC
12:00~ 12:15	Wei Liu, Xuecong Lu, Yuxi Mao, Bing Li (Shenzhen University, China)

Friday, October 27, 10: 15 – 12: 15

Session B6: Reliability
Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Kazutoshi Kobayashi, Kyoto Institute of Technology, Japan

	Title
B6-1	0204: Design for EMI Immunity and ESD Protection for Wearable and Flexible

	ICs (invited)
10:15~	Xunyu Li, Weiquan Hao, Zijin Pan, Runyu Miao, Albert Wang (University of
10:45	California, USA)
B6-2	0290: A 2D Clock Interconnect Electromigration-Thermal Coupling Simulation
D0-2	Method Based on COMSOL
10:45~	Hongahaa Zhang Vanfun Zua (Cauthaget University Ching)
11:00	Hongchao Zhang, Yunfun Zuo (Southeast University, China)
D.(2	0322: Enhancing Temperature Immunity of Digital Circuit Against Aging : The
B6-3	Standard Cell Subset Method
11:00~	Mingyue Zheng, Wangyong Chen, Yaoyang Lyu, Haifeng Chen, Jiahui Chen, Linlin
11:15	Cai (Sun Yat-sen University, China; Guangdong Provincial Key Laboratory of
11.13	Optoelectronic Information Processing Processing Chips and Systems, China)
D.(/	0361: Design of a Low Temperature Drift High Power Supply Rejection Bandgap
B6-4	Reference Circuit
11:15~	Inabyi Va Danavia Maa Wantaa 7hana (Ninaba Uninasita China)
11:30	Junhui Ye, Dongyin Mao, Wentao Zheng (Ningbo University, China)

Friday, October 27, 10: 15 – 12: 15

Session C6: Photo Electron Device
Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Anquan Jiang, Fudan University, China

	Title
C6-1	0239: Ultra-flexible organic photovoltaics for powering wearable
	electronics(invited)
10:15~	Sixing Xiong, Kenjiro Fukuda, Takao Someya (RIKEN, Japan; The University of
10:39	Tokyo, Japan)
CC 2	0507: UTBB Based Photoelectric Field Effect Transistors for In-Sensor
C6-2	Computing (invited)
10:39~	Viceyon Liv (Believe Heimenite, Ching)
11:03	Xiaoyan Liu (Peking University, China)
C6-3	0512: Nanoscale Photodetectors for Infrared Sensing and Intelligent Recognition
C0-3	(invited)
11:03~	Weida Hu (Shanghai Institute of Technical Physics, China)
11:27	
C(1	0286: An Active Pixel Sensor Array based on Compact Photoelectron In-situ
C6-4	Sensing Device (PISD)
11:27~	Jiuhe Wang, Jian Liu, Yong Xu, Yulong Jiang, Jing Wan (Fudan University, China;

11:39	Nanjing University of Posts and Telecommunication, China)	
C6-5	0296: Comparisons of Photodiodes Based on Bulk-Silicon and	
C0-5	Silicon-on-Insulator Substrates	
11:39~	Siyuan Li, Yong Xu, Jing Wan (Fudan University, China; Nanjing University of Posts	
11:51	and Telecommunication, China)	
06.6	0346: Photoelectron In-situ Sensing Device with embedded photodiode and	
C6-6	interface passivation	
11:51~	Yaoru Qu, Jian Liu, Yong Xu, Yulong Jiang, Jing Wan (Fudan University, China;	
12:03	Nanjing University of Posts and Telecommunications, China)	
G (•	0364: Bi ₂ O ₂ Se/P3HT Heterotransistors for Broadband Photodetections with High	
C6-7	Rhotoresponsivities of 10 ⁶ A/W	
12:03~		
12:15	Xilin Lai, Lei Xu, Shuo Liu, Junling Liu, Ming He (Peking University, China)	

Friday, October 27, 10: 15 – 12: 15

Session D6: Process
Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. You Yin, Gunma University, Japan

	Title
D6-1	0483: Selective Atomic Layer Deposition To Extend Moore's Law And Beyond:
D0-1	Surface Kinetic Tuning for Self-Aligned Growth (invited)
10:15~	Jin Yan, Kun Cao, Eryan Gu, Huilong Zhou, Rong Chen (Huazhong University of
10:42	Science and Technology, China)
D6-2	0497: A Future Analysis of The Forbidden Pitch In Photolithography In
D0-2	Advanced Technology Nodes (invited)
10:42~	Yanli Li (Fudan University, China)
11:09	Tami El (Fudan University, China)
D6-3	0505: Noncontact Remote Doping for High-performance Two-dimensional
Ъ0-3	Electronics(invited)
11:09~	Po-Heng Pao, Ren-Hao Cheng, Yi-Hsiu Huang, Yu-Ying Yang, Tzu-Hsien Sang,
11:36	Chia-Ming Tsai, Chao-Hsin Chien (Yang-Ming Chiao-Tung University, Taiwan,
11.30	China)
D6-4	0513: Improved BEOL Design Rules With 45-Degree Local Interconnection
D0-4	(invited)
11:36~	Vienko I in (Fudan University China)
12:03	Xianhe Liu (Fudan University, China)

D6-5	0357: Controllable Growth of P3HT Single-Crystal Films for Organic Field-Effect Transistors
12:03~ 12:15	Chunyao Zhao, Xilin Lai, Ming He (Peking University, China)

Friday, October 27, 13: 30 – 15: 30

Friday, October 27, 13: 30 – 15: 30 Hall 209

Session A7: RF Circuit I Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Yumei Huang, Fudan University, China

	Title
A7-1	0241: Concurrent Multiband CMOS Low Noise Amplifier Design for Internet of
A/-1	Things Applications(invited)
	Peerapat Phetpadriew, Bharatha Kumar Thangarasu, Nagarajan Mahalingam,
13:30~	Zhenghao Lu, Cher Ming Tan, Kiat Seng Yeo (Singapore University, Singapore;
13:58	Tianjin University, China; Soochow University, China; Chang Gung University,
	Taiwan, China)
A7-2	0506: High-Speed, Low-Power, and Small-Area Optical Receiver in 65-nm
117 2	CMOS (invited)
13:58~	Akira Tsuchiya, Toshiyuki Inoue, Keiji Kishine, Daisuke Ito, Yasuhiro Takahashi,
14:26	Makoto Nakamura (The University of Shiga Prefecture, Japan; Gifu University,
14.20	Japan)
A7-3	0336: A Compact 7-10GHz GaN Low Noise Amplifier MMIC with Sub 0.3 dB
A/-3	Gain flatness
14:26~	Shuoxiong Yang, Qingyang Dong, Wei Huang, Xin Jiang, Yang Wang, Weijun Luo
14:39	(University of Chinese Academy of Sciences, China)
A7-4	0355: A 27-to-65-GHz CMOS Amplifier with Tunable Frequency Response
14:39~	Leshan Xu, Shunsuke Yabuki, Satoshi Tanaka, Takeshi Yoshida, Minoru Fujishima
14:52	(Higashihiroshima University, Japan)
	0392: A 4.7-to-18-GHz Ultra-Wideband Variable-Gain Balun-LNA Using
A7-5	3 rd -order-Band-Pass Input Matching in 40-nm CMOS
14:52~	Sicheng Han, Xueyin Wu, Wei Li, Yun Wang, Yue Lin, Hongtao Xu (Fudan
15:05	
15.05	University, China; ICLegend Micro, China)
	0440: A 400M-510MHz On-Chip Transformer-Based RF Power Amplifier with
A7-6	22.5dBm Output Power and 48% PAE
15:05~	Chaoyang Zheng, Zhipeng Chen, Jianhua Lu, Yan Ma, Yumei Huang, Zhiliang Hong

15:18	(Fudan University, China; Beijing Smartchip Microelectronics Technology Co., Ltd;
	China; Beijing Smartchip Semiconductor Technology Co., Ltd, China)
A7-7	0274: A 7W,2.5-5GHz Wideband GaN PA with Transformer-Based Matching
	Network
15:18~	Xiaohan Zhang, Tao Wang, Lingyun Shi, Di Hua, Zhiliang Hong (Fudan University,
15:30	China)

Friday, October 27, 13: 30 – 15: 30

Hall 202

Session B7: NVM I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Viktor Sverdlov, Institute for Microelectronics, TU Wien,

Austria

	Title
B7-1	0255: Stochastic Computing Based on Volatile Ovonic Threshold Switching
	Devices (invited)
13:30~	Zhen Chai, Weidong Zhang, Jianfu Zhang (Livepool John Moores University, United
13:54	Kingdom; Xi'an Jiaotong University, China)
B7-2	0302: Doped Chalcogenides for High-Performance Phase Change Devices
D/-2	(Invited)
13:54~	You Yin (Gunma University, Japan)
14:18	100 1 m (Ounma Oniversity, Japan)
B7-3	0369: Development of 3D Resistance Memory with Multi-level Operation:
D 7-3	Demonstration of QLC and Perspective (invited)
14:18~	Steve S. Chung (Yang Ming Chiao Tung University, Taiwan, China)
14:42	Steve S. Chang (Tang Ming Chao Tang Oniversity, Taiwan, China)
B7-4	0504: Numerical Characterization of a 5-Layer(Pt/Ta/TaO/AlO/W) RRAM
D/-4	Device(invited)
14:42~	Jiahao Li, Wanlan Yang, Xing Zhou (Nanyang Technological University, Singapore)
15:06	Jianao Li, Wanian Tang, Aing Zhou (Nanyang Technological University, Singapore)
B7-5	0524: Device-architecture Co-optimization for RRAM-based In-memory
B 7-3	Computing (invited)
15:06~	Yimao Cai, Yi Gao, Zongwei Wang, Lin Bao, Ling Liang, Qilin Zheng, Cuimei Wang,
15:30	Ru Huang (School of Integrated Circuits, Peking University, China; Beijing Advanced
13.30	Innovation Center for Integrated Circuits, China)

Friday, October 27, 13: 30 – 15: 30

Hall 203

Session C7: Advanced Device & DTCO I

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Sixing Xiong, RIKEN, Japan

	Title
C7-1	0372: TCAD Study on Strain Engineering in Vertical Channel Gate-all-around
	Transistor (invited)
13:30~	Ran Bi, Jianhuan Wang, Haixia Li, Baotong Zhang, Jianjun Zhang, Ming Li (Peking
13.50~	University, China; Chinese Academy of Sciences, China; Beijing Academy of
13.37	Quantum Information Sciences, China)
C7-2	0498: The Impact of Strain and Layout Dependent Effects on High Frequency
C1-2	Performance and Low Frequency Noise in Nanoscale Devices (invited)
13:57~	Jyh-Chyurn Guo, Chih-Shiang Chang (Yang Ming Chiao Tung University, Taiwan,
14:24	China)
C7-3	0509: A Simple New Line-Tunneling iTFET with Overlapping Between Gate and
C/-3	Source Contact (invited)
14:24~	Jyi-Tsong Lin, Kuan-Pin Lin (Sun Yat-Sen University, Taiwan, China)
14:51	3yi-1song Em, Kuan-i in Em (Sun Tui-Sen University, Tuiwan, China)
C7-4	0514: Nanodevices for The End of The Roadmap (invited)
14:51~	Francis Balestra (IMEP-LAHC, France)
15:18	Trancis Baiesua (IMEF-LATIC, France)
C7-5	0510: Steeper Subthreshold Swing Attained in Ge-Source Inductive Tunneling
C/-5	FET via Epitaxial Tunnel Layer for Suppressed Point Tunneling
15:18~	Yen-Chen Chang, Wei-Heng Tai, Jyi-Tsong Lin (Sun Yat-Sen University, Taiwan,
15:30	China)

Friday, October 27, 13: 30 – 15: 30

Session D7: MEMS

Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Rong Chen, Huazhong University of Science and
Technology, China

Hall 207

	Title
D7-1	0244: 3D MEMS Devices Fabricated On Ultrathin Cylindrical Substrate for
	Flexible Wearable Applications (invited)
13:30~	
14:00	Zhuoqing Yang (Shanghai Jiaotong University, China)
D7-3	0492: Intelligent Multimodal Sensors Based on Novel Electronic-Ionic Bi ₂ O ₂ Se
	Semiconductors (invited)
14:00~	Xinrui Guo, Lei Xu, Qifeng Cai, Shuo Liu, Junling Liu, Ming He (Peking University,

14:30	China)
D7-4	0519: Flexible Sensing Materials And Devices (invited)
14:30~	Oissa 71-a (Nesiisa Haissasita of Barta and T.L. and S. China
15:00	Qiang Zhao (Nanjing University of Posts and Telecommunications, China)
D7.5	0408: Highly Reliable Physical Unclonable Function Based on ZnO-SnO ₂ Gas
D7-5	Sensor
15:00~	Haonan He, Pengjun Wang, Xiangyu Li, Li Ni, Yuejun Zhang (Ningbo University,
15:15	China; Wenzhou University, China)

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Hall 209

Session A8: RF Circuit II

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Akira Tsuchiya, The University of Shiga Prefecture, Japan

	Title
A8-1	0523: Development of RF CMOS Technologies in the 1990s in Toshiba (invited)
15:45~	Histori (Vana Mina Chan Tana Hairanaita Tairana China)
16:15	Hiroshi Iwai (Yang Ming Chao Tung University, Taiwan, China)
A8-2	0267: A Dual-Core Quad_Mode VCO with Reconfigurable Magnetic Coupling
A0-2	Mode and Negative-Resistive Mode Switch
16:15	Xiangjian Kong, Kai Xu, Qing Qiu, Mingchao Jian, Chunbing Guo (Guangdong
16:30	University of Technology, China; King's College London, The United Kingdom)
40.2	0299: A 293-to-303 GHz Fundamental VCO with -4dBm Peak Output Power in
A8-3	40nm CMOS
16:30	Sanglai Mang Ziyang Dang Yun Wang Hangtoo Yu (Endan Haiyangita Ching)
16:45	Songlei Meng, Ziyang Deng, Yun Wang, Hongtao Xu (Fudan University, China)
A8-4	0307: Suppression of Reflections and Elimination of Transmission Disparities in
A0-4	Differential Crossover Line Junctions
16:45~	Zhen Yan, Satoshi Tanaka, Takeshi Yoshida, Minoru Fujishima (Hiroshima University,
17:00	Japan)
A8-5	0345: A High Speed, Low Power and Low Phase Noise Divider for Wideband
A0-5	Application
17:00~	Xinyi Lin, Dejian Li, Hao Xu, Na Yan (Fudan University, China; Beijing Smartchip
17:15	Semiconductor Technology Co., Ltd, China)

A8-6	0394: A Compact 144% Fractional Bandwidth CMOS Power Amplifier With an
	Optimization of Synthesized High-Order Matching Network
17:15~	Yunhao Li, Wei Li, Yun Wang, Wei Luo, Yue Lin, Hongtao Xu (Fudan University,
17:30	China; ICLegend Micro, China)

Friday, October 27, 15: 45 – 17: 45

Hall 202

Session B8: NVM II

Platinum Hanjue Hotel 2nd Floor

Session Chair: Prof. Steve Chung, Yang Ming Chiao Tung University, Taiwan,

China

	Title
B8-1	0226: Fatigue-Free Ferroelectric Domain Wall Memory (invited)
15:45~	Anquan Jiang (Fudan University, China)
16:09	Anquan Hang (Futur Oniversity, China)
B8-2	0376: Flash-based Computing-in-memory Architectures with High-accuracy and
D0-2	Robust Reliabilities for General-purpose Applications (invited)
16:09~	Yang Feng, Yueran Qi, Xuepeng Zhan, Jixuan Wu, Jiezhi Chen (Shandong University,
16:33	China)
B8-3	0494: Charge and Spin Transport in Semiconductor Devices (invited)
16:33~	Viktor Sverdlov, Siegfried Selberherr (TU Wien Vienna, Austria)
16:57	Viktor Sverdrov, Siegined Sciocifici (10 wien vienna, Austria)
B8-4	0532: Overcoming the challenges of ReRAM towards mass production from the
D0-4	perspectives of process, design and application (invited)
16:57~	Yefan Liu, Yunfeng Wu, Liang Chen, Polaron Cao, Yuliang Zhou, Vincent Zhang
17:21	(Innostar Inc, China)
B8-5	0278: ReMap: Reorder Mapping for Multi-level Uneven Distribution on Sparse
D0-3	ReRAM Accelerator
17:21~	Zhuo Chen, Zihan Zhang, Jianfei Jiang, Weiguang Sheng, Qin Wang, Naifeng Jing
17:33	(Shanghai Jiaotong University, China)
B8-6	0377: One-shot Read Processing to Enhance Cold Data Retention in Charge-trap
D0-0	TLC 3D NAND Flash
17:33~	Shaoqi Yang, Xiaohuan Zhao, Kenie Xie, Xuepeng Zhan, Jixuan Wu, Jiezhi Chen
17:45	(Shandong University, China)

Friday, October 27, 15: 45 – 17: 45

Hall 203

Session C8: Advanced Device & DTCO II Platinum Hanjue Hotel 2nd Floor **Session Chair: Prof. Chai, Zheng, Xi'an Jiaotong Univeristy, China**

	Title
C8-1	0432: Hybrid Tunnel FET-CMOS Foundry Platform With Ultra-Low Leakage
	for Power-Constraint And Energy-Efficient Application (invited)
15:45~	Qianqian Huang (Peking University, China)
16:12	Qianqian Tuang (1 eking University, China)
C8-2	0496: Corner Rounding, What Can We Expect In Optical Microlithography (invited)
16:12~	Oiona Wu (Eudan University China)
16:39	Qiang Wu (Fudan University, China)
C8-3	0534: Advanced Semiconductor Device Modeling: Status Challenge and
C0-3	Opportunity (invited)
16:39~	Yutao Ma (Primarius Technologies Co., Ltd., China)
17:06	Tutao Ma (17tmarius Technologies Co., Lia., China)
C8-4	0326: Matching Learning-Assisted Single-Event Transient Model of 12nm
C0-4	FinFETs for Circuit-Level Simulation
17:06~	Jianwen Lin, Linlin Cai, Yutao Chen, Haoyu Zhang, Wangyong Chen (Sun Yat-Sen
17:19	University, China)
C8-5	0359: A Continuous and Closed-form Trans-Capacitance Model for Double-Gate
C0-3	Junctionless Transistors
17:19~	Xingchen Xin, Chunsheng Jiang, Hongying Chen (Guangxi Normal University,
17:32	China)
C8-6	0511: An iTFET with Control Gate for Low Power Applications in RF and Digital Circuits
17:32~	Ho-Hin Tse, Zheng-Hong Zhong, Jyi-Tsong Lin (Sun Yat-Sen University, Taiwan,
17:45	China)

Friday, October 27, 15: 45 – 17: 45

Session D8: Testing
Platinum Hanjue Hotel 2nd Floor
Session Chair: Prof. Shunli Ma, Fudan University, China

	Title
D8-1	0225: Signal Generation Technologies for Analog/Mixed-Signal IC Testing (invited)
15:45~ 16:15	Haruo Kobayashi (Gunma University, Japan)
D8-2	0232: Extracting statistical distributions of RTN originating from both acceptor-like and donor-like traps (invited)

16:15~	Kean H. Tok, Jian F. Zhang, James Brown, Zhigang Ji, Weidong Zhang (Livepool
16:45	John Moores University, United Kingdom; Shanghai Jiaotong University, China)
D8-3	0453: In Situ Device and System (invited)
16:45~	Shiyi Zhang, Xinyue Zheng, Mingyang Zhang, Zuoyuan Dong, Lan Li, Xiaomei Li,
17:15	Xing Wu (East China Normal University, China)
D8-4	0209: Receiver Characterization with On-Die Eye Monitor (ODEM) in LPDDR5
	and DDR5 SDRAM
17:15~	Feng (Dan) Lin, Kang (Leo) Zhao (Changxin Memory Technologies, China)
17:30	
D8-5	0382: Ring Oscillators with identical Circuit Structure to Measure Bias
	Temperature Instability
17:30~	Daisuke Kikuta, Ryo Kishida, Kazutoshi Kobayashi (Kyoto Institute of Technology,
17:45	Japan; Toyama Prefectural University, Japan)